

AMENDMENTS TO THE CLAIMS

The following is a complete listing of revised claims with a status identifier in parenthesis.

LISTING OF CLAIMS

Claims 1-8 (Canceled)

9. (Currently Amended) A toggle flip-flop circuit comprising
a bistable circuit element having first and second output terminals exhibiting respective complementary first and second binary output voltages, and

triggering means for ~~selectively coupling transitions of a predetermined polarity in a binary input signal to said bistable circuit element to initiate~~ initiating a change of state for said bistable circuit element from a present output state to a complementary output state, said change of state causing both said first and second terminals to change binary output voltages,

said triggering means comprising

a first capacitor having a first terminal connected to a reference voltage and a second terminal connected through a first switch to said first output of said bistable circuit element,

a second capacitor having a first terminal connected to a reference voltage and a second terminal connected through a second switch to said second ~~putout~~ output of said bistable circuit element,

said first and second switches controlled by said binary input signal such that

when said binary input signal is in a first state said switches connect said ~~first~~ second terminal of respective ones of said capacitors to respective outputs of said bistable circuit element,

when said binary input signal is in a second state said switches disconnect said capacitors from said respective outputs of said bistable circuit element,

means responsive to ~~said voltage~~ voltages on said capacitors ~~and said transitions of said predetermined polarity in said binary input signal for~~ switching the binary state of said bistable circuit element.

10. (Currently Amended) The toggle flip-flop circuit of claim [[1]] 9 wherein said bistable circuit element comprises first and second inverters, each inverter having an input terminal and an output terminal, output terminals of each inverter being connected to the input terminal of the other inverter, output terminals of each of said inverters corresponding to respective ones of said output terminals of said bistable circuit element.

11. (Currently Amended) The toggle flip-flop circuit of claim [[2]] 10 wherein said means for switching the binary state of said bistable circuit element comprises means for connecting the output terminal of one of said inverters to said reference voltage.

12. (Original) The toggle flip-flop circuit of claim 11 wherein said switches comprise pass transistor configurations responsive to said input signal and its complement.

13. (Currently Amended) The toggle flip-flop circuit of claim ~~[[4]]~~ 12 further comprising means for inhibiting transitions in said input signal ~~of other than predetermined polarity~~ from affecting said means for connecting.

14. (Original) The toggle flip-flop circuit of claim 13, wherein said means for inhibiting comprises a transistor connected in parallel across said capacitors operating in response to a constant applied voltage.

15. (Original) The toggle flip-flop circuit of claim 9 wherein said reference voltage is ground.

16. (Original) The toggle flip-flop circuit of claim 9 further comprising means for resetting said bistable circuit element to an initial state.

17. (Currently Amended) The toggle flip-flop circuit of claim 16 wherein said means for resetting comprises a switch selectively connecting at least one of said output terminals to ~~respective said~~ reference voltage ~~voltage~~.